

#13

PATENT

740756-2101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of: ) Art Unit: 2813  
Shunpei YAMAZAKI et al. ) Examiner: E. Pert  
Application No.: 09/502,675 )  
Filed: February 11, 2000 )  
For: SEMICONDUCTOR DEVICE )  
METHOD OF MANUFACTURING )  
THEREFOR )

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents  
Washington, D.C. 20231

Dear Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

Full English translations for Japanese Patent Nos. 5-102483 and 4-369271 are also submitted herewith.

It is requested that the accompanying information disclosure statement be considered and made of record in the above-captioned application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

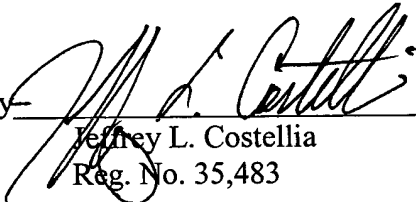
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JE FC:126

180.00 DP

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By   
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JLC/RLP:gjc

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Attachments: 3 U.S. Patents  
2 Japanese Patents with Full English translations